

**About the Institute**

VikasCollege of Engineering and Technology  
Established in The Year 2010 One of The Best Known  
Self-Financing Integrated Institutions Aiming to  
Provide Excellence in Engineering, And Management  
Studies. It Is Governed by Mother Theresa Educational  
Society to Encourage Aspiring Students with Very  
Good Academic Record. It Offers Undergraduate  
Engineering (CIVIL, EEE, MECHANICAL,  
ECE,CSE, AGRICULTURE, CSE(AI&ML),  
CSE(DS)) Along With PG M.Tech(CSE, VLSID,  
DECS, EPS, SE AND TE). MBA. It Has Strong  
Industry- Institute Partnership, Entrepreneurship Cell  
and R&D Cell to Enhance the Practical Knowledge of  
Students.

**Program objective:**

The main of this training program to expose the  
participants to various power transmission techniques,  
applied to contemporary problems in the field of  
mechanical engineering

**Chief patrons**

**Sri. N. NARSI REDDY**  
Chairman

**Sri. N.S. REDDY**  
Secretary & Correspondent

**PATRON:**  
**Dr.B. RAMANA. Ph.d**

**CONVENER**  
**B L NAYAK**  
**HOD**  
**EEE**

**COORDINATOR**  
**K SREENIVASA RAO**  
**Asst.Prof**  
**EEE**

**Organizes**

A one day seminar on  
"Research and development on low power techniques in  
VLSI design"

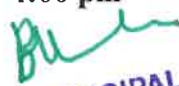
**Resource Person:**

**M Ganesh**  
Asst.professor  
Ece

**Date: 11-11-2023**

**Venue: Seminar Hall**

**Time: 10:00 am to 4:00 pm**

  
**PRINCIPAL**  
**VIKAS COLLEGE OF ENGG. TE**  
**NUNNA - 521 212**  
**Vijayawada Rural, NTR Dist..**



## Learning out comes

1. Describe the fabrication process and properties of MOS devices
2. Analyze the impact of scaling on MOS circuits
3. Comprehend the need of hardware description language and its features.
4. Reduce energy consumption resulting in extended battery life for portable devices and mitigated heat dissipation for large-scale systems
5. continually formulate national goals for a sustained and vibrant VLSI industry

Research and development on low  
power techniques in VLSI design

JMHF+FMW, Teachers Colony, Andhra Pradesh 521229, India

Latitude

16.6278793°

Longitude

80.6740289°

Local 05:18:52 PM

GMT 11:48:52 AM

Altitude 122 meters

Monday, 11.11.2023

PRINCIPAL  
VIKAS COLLEGE OF ENGG. TECH.  
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**B NAYAK**  
HOD  
EEE

**COORDINATOR**  
**K SREENIVASA RAO**  
Asst.Prof  
EEE



Organizes

A one day seminar on  
“research development programme”


**Resource Person:**

Sujith S  
Asst.Prof  
EEE

**Date: 17-12-2023**

**Venue: Seminar Hall**

**Time: 10:00 am to 4:00 pm**

  
**PRINCIPAL**  
**VIKAS COLLEGE OF ENGG. TI**  
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## Learning out comes

1. program that helps researchers develop skills to improve their effectiveness and employability
2. R&D is the process of developing new products or services, or improving existing ones
3. R&D helps companies stay ahead of competitors by meeting new market needs

## Examples of R&D activities

- Basic research
- Applied research
- Developing new products
- Improving processes, services, or materials
- Using prototypes

